

20/553902

JC20 Rec'd PCT/PTO 21 OCT 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Hiroshi AZAKAMI et al. : **Mail Stop: PCT**
Serial No. NEW : Attorney Docket No. 2005_1601A
Filed October 21, 2005 :
HIGH FREQUENCY SIGNAL LEVEL :
DETERMINING DEVICE AND HIGH :
FREQUENCY SIGNAL RECEIVER :
APPARATUS USING THE SAME :
[Corresponding to PCT/JP2004/005707
Filed April 21, 2004] THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

SUBMISSION OF REPLACEMENT DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

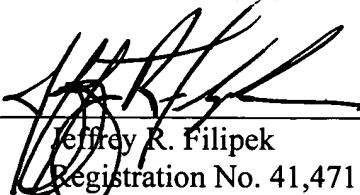
Sir:

Submitted herewith are 6 sheets of formal (A4) drawings (Figs. 1, 11, 16, 20, 23, and 24). As described in the Preliminary Amendment filed concurrently herewith, in the corrected drawings various editorial amendments have been made. Due to the nature and number of changes, a marked-up of the amended figures are enclosed in order to provide an explanation of the amendments.

Respectfully submitted,

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By


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ATTACHMENT F

Fig.1

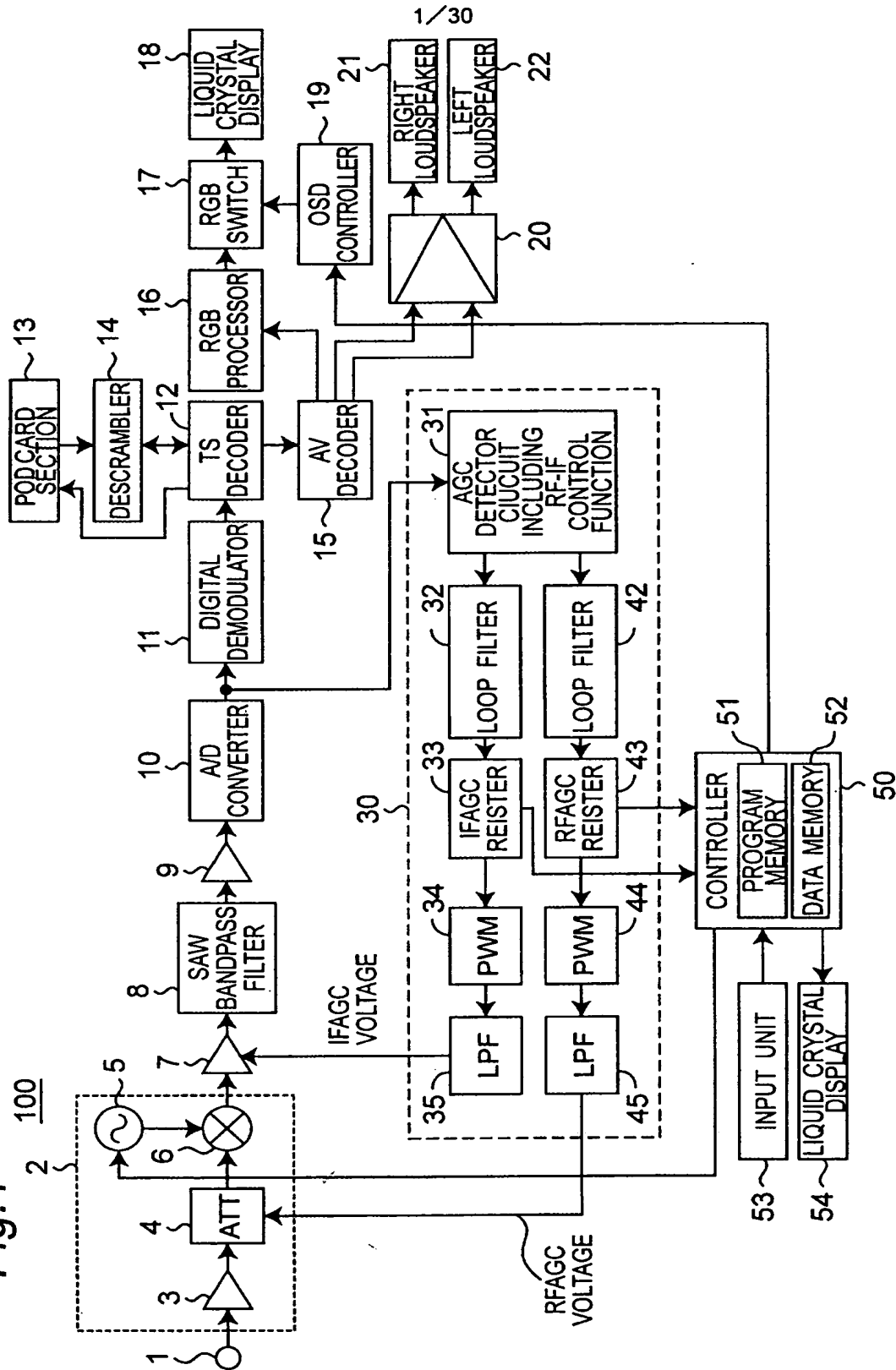


Fig. 11

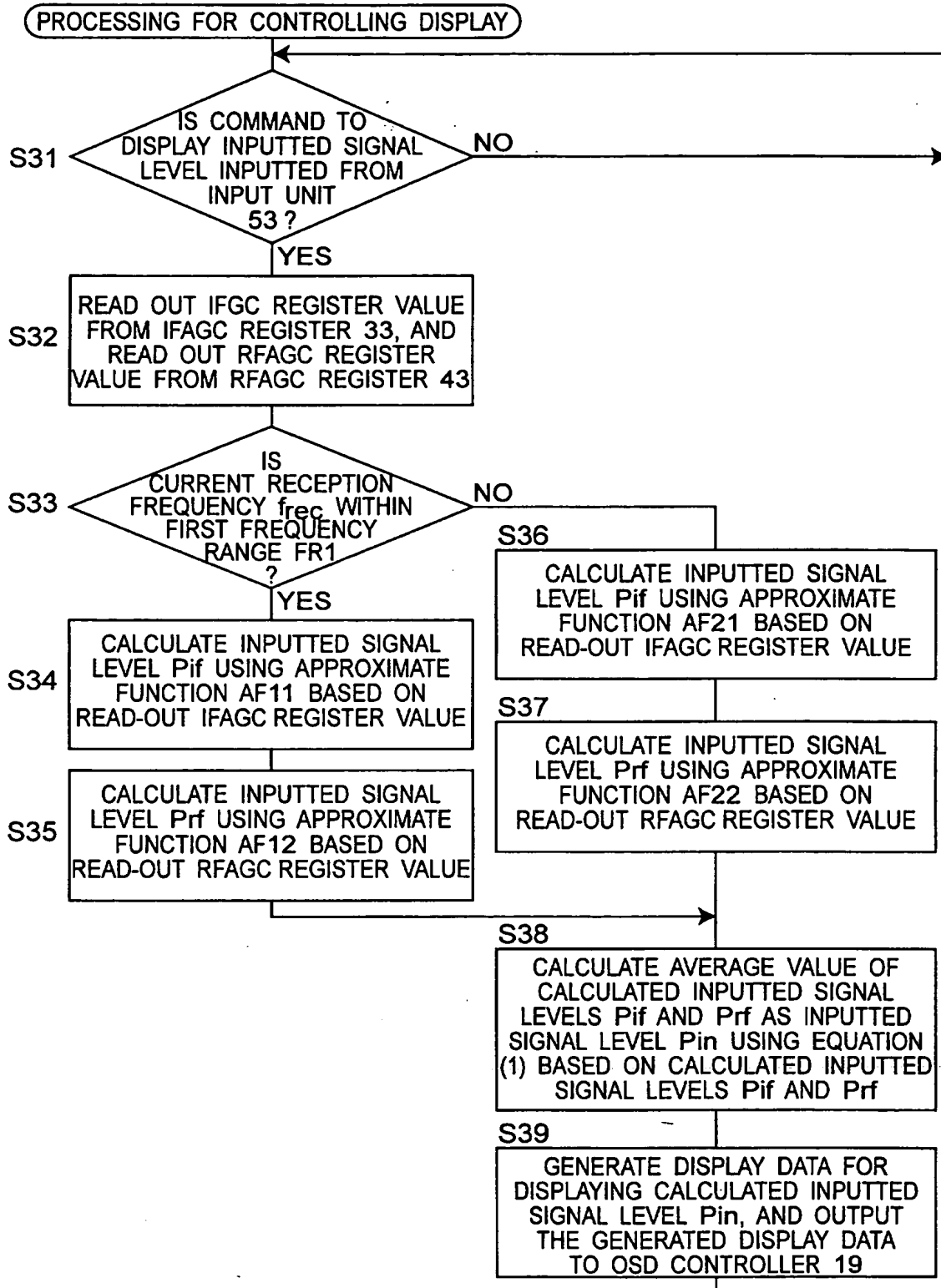


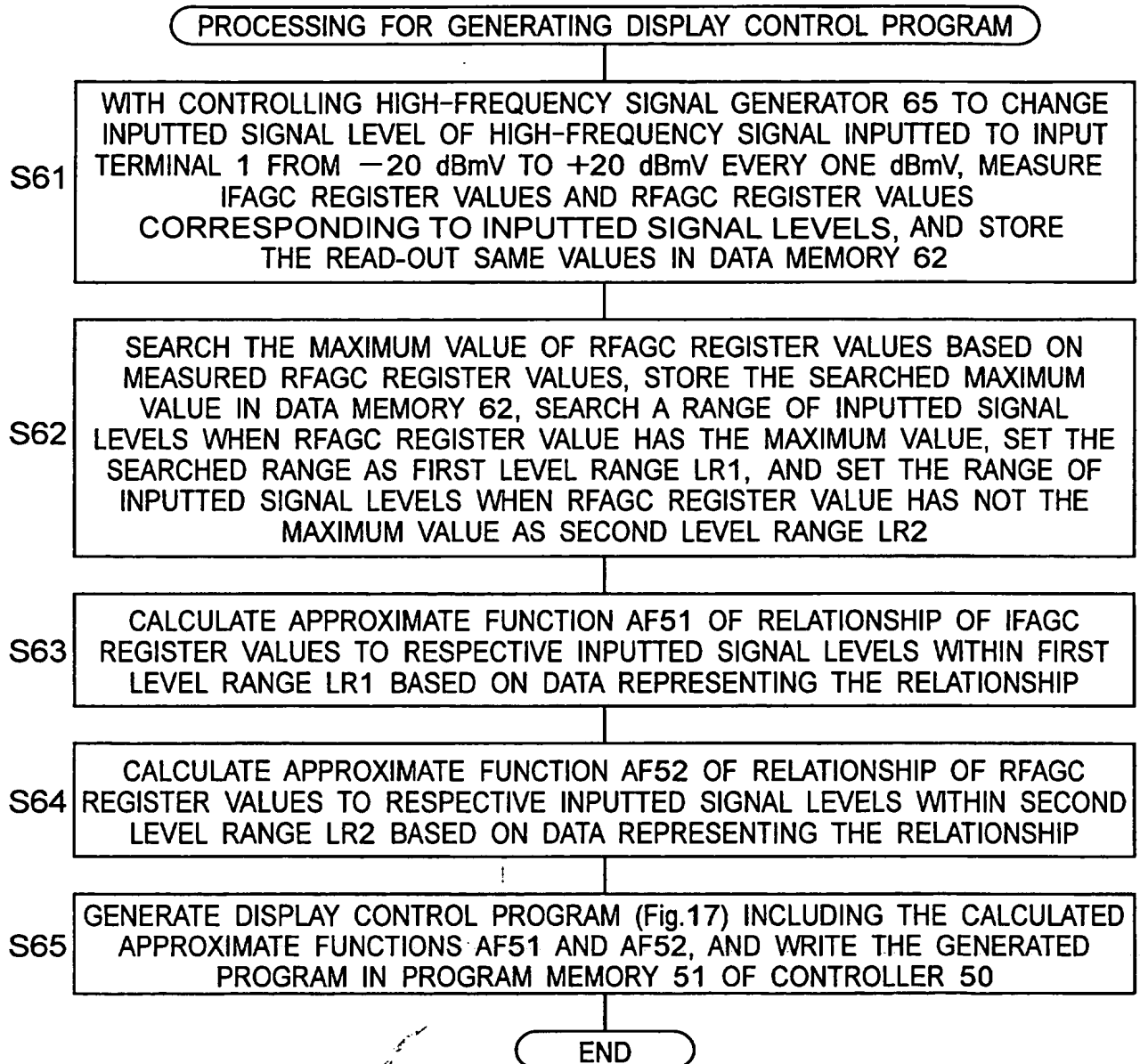
Fig. 16

Fig.20

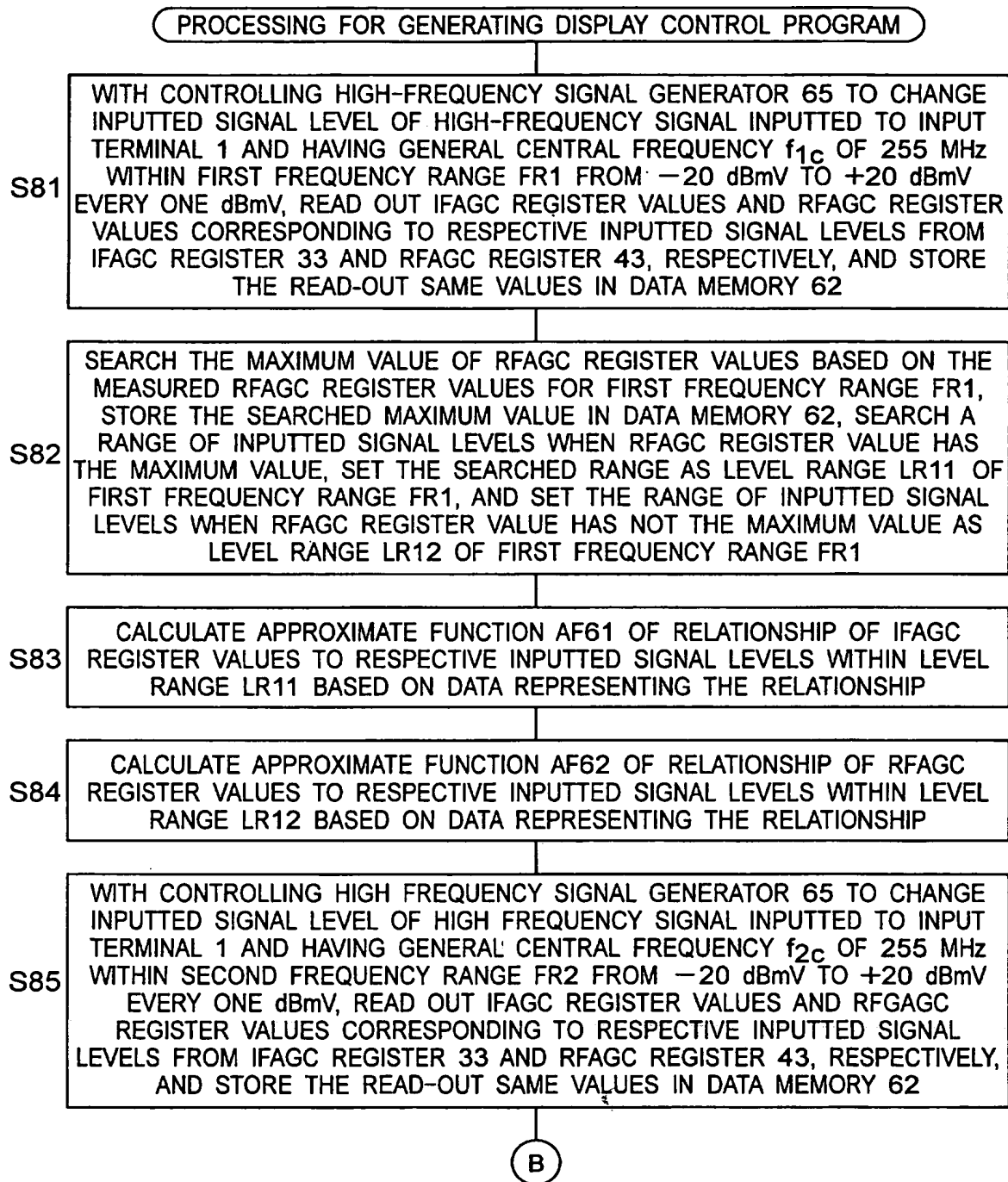


Fig.23

PROCESSING FOR GENERATING DISPLAY CONTROL PROGRAM

- S101 WITH CONTROLLING HIGH-FREQUENCY SIGNAL GENERATOR 65 TO CHANGE INPUTTED SIGNAL LEVEL OF HIGH-FREQUENCY SIGNAL INPUTTED TO INPUT TERMINAL 1 AND HAVING MINIMUM FREQUENCY f_{1min} OF 57 MHz WITHIN FIRST FREQUENCY RANGE FR1 FROM -20 dBmV TO $+20$ dBmV EVERY ONE dBmV, READ OUT IFAGC REGISTER VALUES AND RFAGC REGISTER VALUES CORRESPONDING TO RESPECTIVE INPUTTED SIGNAL LEVELS FROM IFAGC REGISTER 33 AND RFAGC REGISTER 43, RESPECTIVELY, AND STORE THE READ-OUT SAME VALUES IN DATA MEMORY 62
- S102 WITH CONTROLLING HIGH-FREQUENCY SIGNAL GENERATOR 65 TO CHANGE INPUTTED SIGNAL LEVEL OF HIGH-FREQUENCY SIGNAL INPUTTED TO INPUT TERMINAL 1 AND HAVING MAXIMUM FREQUENCY f_{1max} WITHIN FIRST FREQUENCY RANGE FR1 AND MINIMUM FREQUENCY f_{2min} OF 459 MHz WITHIN SECOND FREQUENCY RANGE FR2 FROM -20 dBmV TO $+20$ dBmV EVERY ONE dBmV, READ OUT IFAGC REGISTER VALUES AND RFAGC REGISTER VALUES CORRESPONDING TO RESPECTIVE INPUTTED SIGNAL LEVELS FROM IFAGC REGISTER 33 AND RFAGC REGISTER 43, RESPECTIVELY, AND STORE THE READ-OUT SAME VALUES IN DATA MEMORY 62
- S103 WITH CONTROLLING HIGH-FREQUENCY SIGNAL GENERATOR 65 TO CHANGE INPUTTED SIGNAL LEVEL OF HIGH-FREQUENCY SIGNAL INPUTTED TO INPUT TERMINAL 1 AND HAVING MAXIMUM FREQUENCY f_{2max} OF 861 MHz WITHIN SECOND FREQUENCY RANGE FR2 FROM -20 dBmV TO $+20$ dBmV EVERY ONE dBmV, READ OUT IFAGC REGISTER VALUES AND RFAGC REGISTER VALUES CORRESPONDING TO RESPECTIVE INPUTTED SIGNAL LEVELS FROM IFAGC REGISTER 33 AND RFAGC REGISTER 43, RESPECTIVELY, AND STORE THE READ-OUT SAME VALUES IN DATA MEMORY 62
- S104 SEARCH THE MAXIMUM VALUE OF RFAGC REGISTER VALUES BASED ON THE MEASURED RFAGC REGISTER VALUES AT MINIMUM FREQUENCY f_{1min} OF FIRST FREQUENCY RANGE FR1, STORE THE SEARCHED MAXIMUM VALUE IN DATA MEMORY 62 AS THE MAXIMUM VALUE OF RFAGC REGISTER VALUES WITHIN FIRST FREQUENCY RANGE FR1, SEARCH A RANGE OF INPUTTED SIGNAL LEVELS WHEN RFAGC REGISTER VALUE HAS THE MAXIMUM VALUE, SET THE SEARCHED RANGE AS LEVEL RANGE LR11 OF FIRST FREQUENCY RANGE FR1, AND SET THE RANGE OF INPUTTED SIGNAL LEVELS WHEN RFAGC REGISTER VALUE HAS NOT THE MAXIMUM VALUE AS LEVEL RANGE LR12 OF FIRST FREQUENCY RANGE FR1
- S105 SEARCH THE MAXIMUM VALUE OF RFAGC REGISTER VALUES BASED ON THE MEASURED RFAGC REGISTER VALUES AT MINIMUM FREQUENCY f_{2min} OF SECOND FREQUENCY RANGE FR2, STORE THE SEARCHED MAXIMUM VALUE IN DATA MEMORY 62 AS THE MAXIMUM VALUE OF RFAGC REGISTER VALUES WITHIN SECOND FREQUENCY RANGE FR2, SEARCH A RANGE OF INPUTTED SIGNAL LEVELS WHEN RFAGC REGISTER VALUE HAS THE MAXIMUM VALUE, SET THE SEARCHED RANGE AS LEVEL RANGE LR21 OF SECOND FREQUENCY RANGE FR2, AND SET THE RANGE OF INPUTTED SIGNAL LEVELS WHEN RFAGC REGISTER VALUE HAS NOT THE MAXIMUM VALUE AS LEVEL RANGE LR22 OF SECOND FREQUENCY RANGE FR2

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Fig.24

